

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of: **Nirio FUKUSAWA et al.**

PATENT APPLICATION

Serial Number: **(Divisional of Appln. Serial No.
09/029,608, filed March 15, 1998)**

Prior Art Unit: 2814 *12-22-00 Hallman*

Filed: Herewith

Prior Examiner: **D. Graybill**

For: **METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR
DEVICE, SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING
THE DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

August 8, 2000

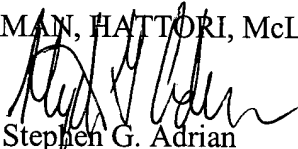
Sir:

This Information Disclosure Statement is being filed in order to comply with applicants' duty of disclosure under 37 CFR §1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/029,608.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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